	Application No.	Applicant(s)
Notice of Allowability	10/701,479	SUNTER ET AL.
	Examiner	Art Unit
	Steve Nguyen	2138
	Toteve Nguyen	2100
The MAILING DATE of this communication app All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85 NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R of the Office or upon petition by the applicant. See 37 CFR 1.31	6 (OR REMAINS) CLOSED in this a ) or other appropriate communication RIGHTS. This application is subject	application. If not included on will be mailed in due course. THIS
1. This communication is responsive to <u>11-2-06</u> .		
2. The allowed claim(s) is/are 1,2,4-7 and 18.		
3. ☐ Acknowledgment is made of a claim for foreign priority u  a) ☐ All b) ☐ Some* c) ☐ None of the:  1. ☐ Certified copies of the priority documents hav	e been received.	
2. Certified copies of the priority documents have been received in Application No		
3. Copies of the certified copies of the priority documents have been received in this national stage application from the		
International Bureau (PCT Rule 17.2(a)).		
* Certified copies not received:		
Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.  THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		
4. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.		
5. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.		
(a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached		
1) 🗌 hereto or 2) 🔲 to Paper No./Mail Date		
(b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date		
Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).		
6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.		
Attachment(s) 1. ⊠ Notice of References Cited (PTO-892)	5. Notice of Informal	Potent Application
. ,		• •
2. Notice of Draftperson's Patent Drawing Review (PTO-948)	Paper No./Mail D	Date <u>1-9-07</u> .
<ul> <li>3.  Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date</li> <li>4.  Examiner's Comment Regarding Requirement for Deposit</li> </ul>	7. ⊠ Examiner's Amen	ment of Reasons for Allowance
of Biological Material		HOLK OF INCOSOTIS FOR ALLOWALICE
	SUI	PERVISORY PATENT EXAMINER

## **EXAMINER'S AMENDMENT**

- 1. Claims 1, 2, 4-7, and 18 are allowed.
- 2. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Eugene Proulx on 1/9/2007.

• Claims 8-17, previously withdrawn from consideration, are hereby cancelled.

## Reasons for Allowance

3. The following is an examiner's statement of reasons for allowance: The present invention pertains to testing pin drivers of an IEEE 1149.1 compliant circuit by selectively controlling a pin driver enable input of the pin drivers for temporarily deasserting a tri-stating signal of the pin drivers.

The prior art of record fails to teach or suggest the novel element of the instant invention. Specifically, the primary reason for allowance is the feature of: a tristate control circuit that temporarily de-asserts a tristate control signal during a capture state of a TAP controller when a first mode control input signal is a logic 1 as expressed in

independent claim 1; and an update control circuit having means for combining a test clock signal and a TAP run-test/idle state signal for producing a delayed update control signal, and means for selecting between a TAP Update-DR state signal and said delayed update control signal for providing an update signal to said BSR as expressed in independent claim 18.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

## Conclusion

4. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Steve Nguyen whose telephone number is (571) 272-7214. The examiner can normally be reached on M-F, 9am-5:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Albert Decady can be reached on (571) 272-3819. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Art Unit: 2138

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Steve Nguyen Examiner Art Unit 2138

SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2100